

10

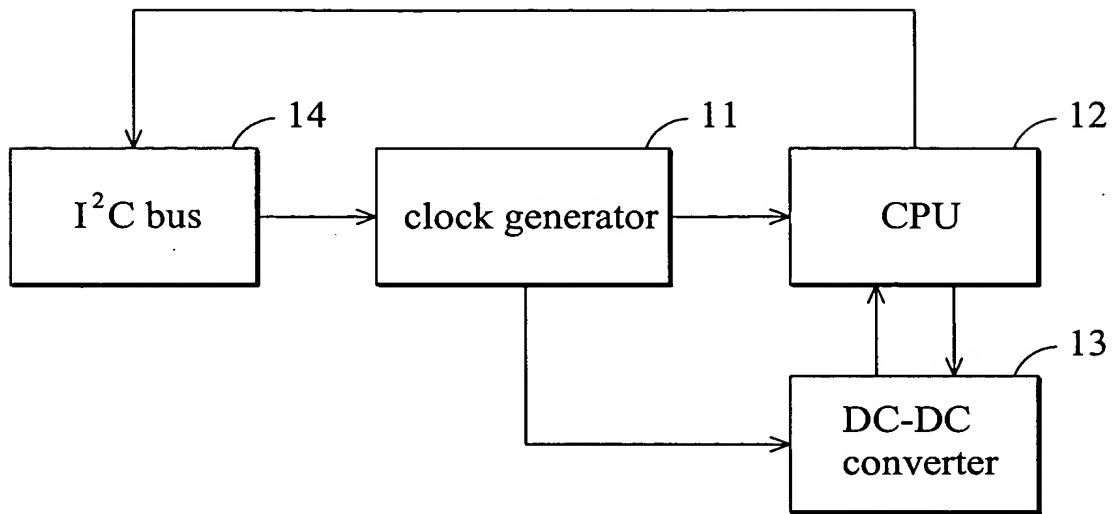


FIG. 1a (PRIOR ART)

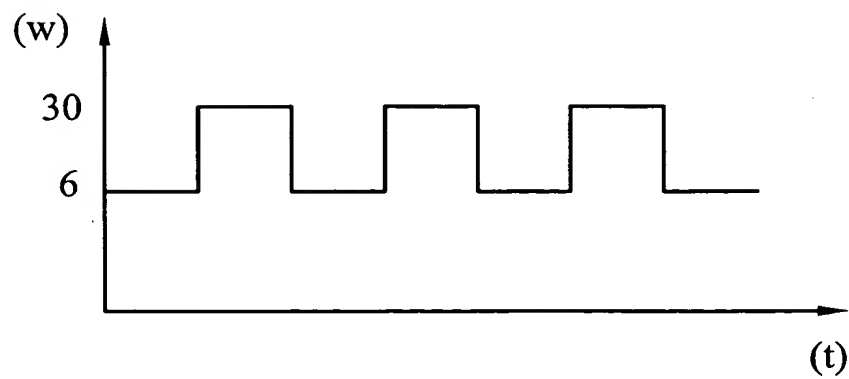


FIG. 1b (PRIOR ART)

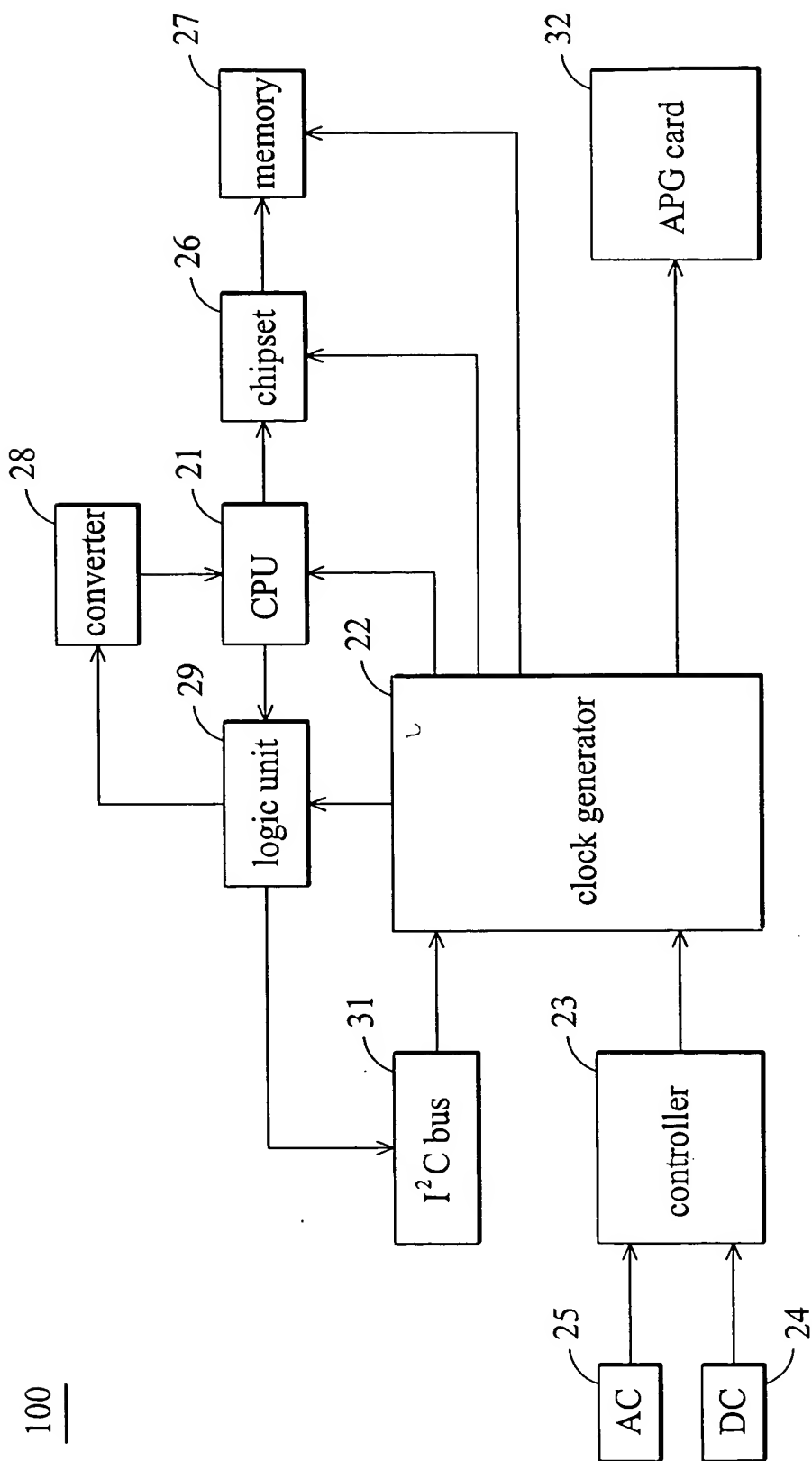


FIG. 2

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	AC				DC			
	CPU1		CPU2		CPU1		CPU2	
over clock	Fa	Va	Fd	Vd	Fg	Vg	Fj	Vj
normal	Fb	Vb	Fe	Ve	Fh	Vh	Fk	Vk
powersaving	Fc	Vc	Ff	Vf	Fi	Vi	Fl	Vl

FIG. 3

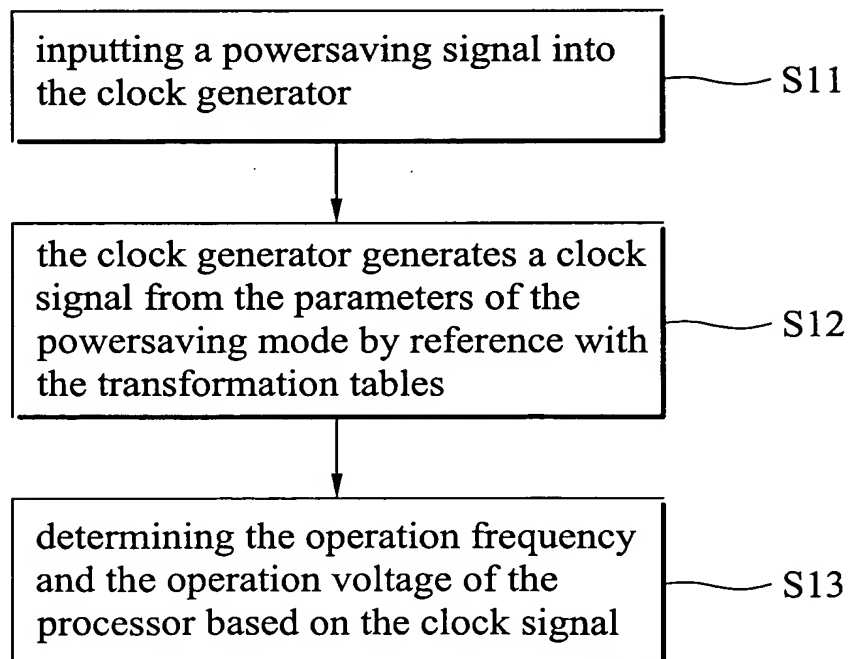


FIG. 4